



MITS

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(Deemed to be University under section 3 of UGC Act, 1956)

MITSU/ES/Time Table/2026-27/04

EXAMINATION SECTION

ACADEMIC YEAR: 2026-27

Time Table - M.Tech. II Year I Semester (R25) - I Mid Term Test -October 2026

TIMINGS: FN: 09:30 A.M to 11:30 AM

AN: 02:30 P.M to 04:30 PM

Date/Day	Session	Computer Science and Engineering (CSE)	VLSI Design and Embedded Systems (VES)
14.10.2026 (Wednesday)	FN	Quantum Computing and Algorithms- 25MBCSETC05	Physical Design Automation-25MBVESTC05
	AN	Automation and Robotics-25MBMECOC01	

Dated: 03.10.2026


CONTROLLER OF EXAMINATIONS

To be read in all the M. Tech-(CSE, VES) II Year I Semester Classrooms

Copy to

The File Website Notice Board HoD-CSE HoD-ECE Transport Incharge

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE - 517 325, A. P.

Note: HoD is requested to circulate among the faculty members concerned